

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Takashi YAMADA, et al.

SERIAL NO: New Application

FILING DATE: Herewith

FOR: SEMICONDUCTOR CHIP HAVING MULTIPLE FUNCTIONAL BLOCKS INTEGRATED IN A SINGLE CHIP AND METHOD FOR FABRICATING THE SAME

FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	28 - 20 =	8	× \$18 =	\$144.00
INDEPENDENT CLAIMS	5 - 3 =	2	× \$84 =	\$168.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)				+ \$280 = \$0.00
<input checked="" type="checkbox"/> LATE FILING OF DECLARATION				+ \$130 = \$130.00
BASIC FEE				\$740.00
TOTAL OF ABOVE CALCULATIONS				\$1,182.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE				+ \$130 = \$0.00
<input type="checkbox"/> RECORDATION OF ASSIGNMENT				+ \$40 = \$0.00
TOTAL				\$1,182.00

- ☐ Please charge Deposit Account No. 15-0030 in the amount of _____ A duplicate copy of this sheet is enclosed.
- ☒ A check in the amount of **\$1,182.00** to cover the filing fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Date: _____

11/29/01

Marvin J. Spivak

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C. Irvin McClelland

Filing Date: 11/29/01

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